

FIG.1

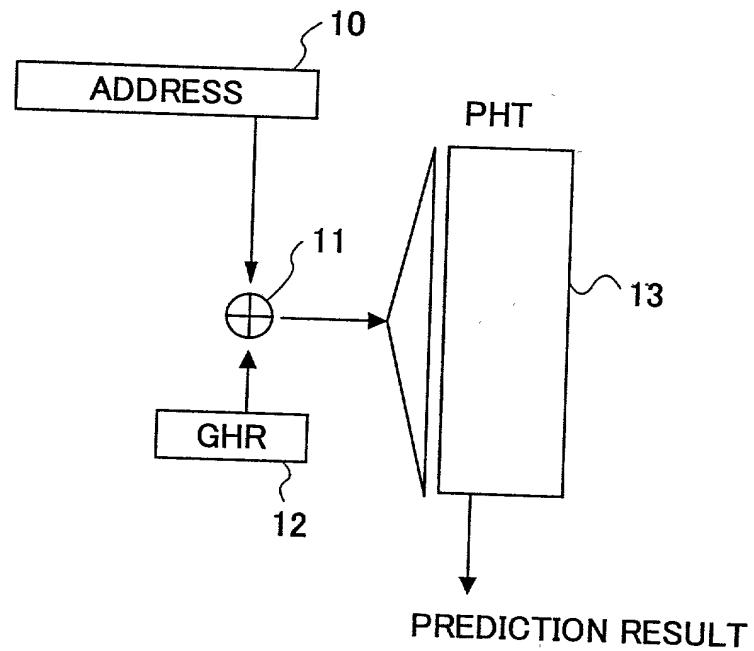


FIG.2

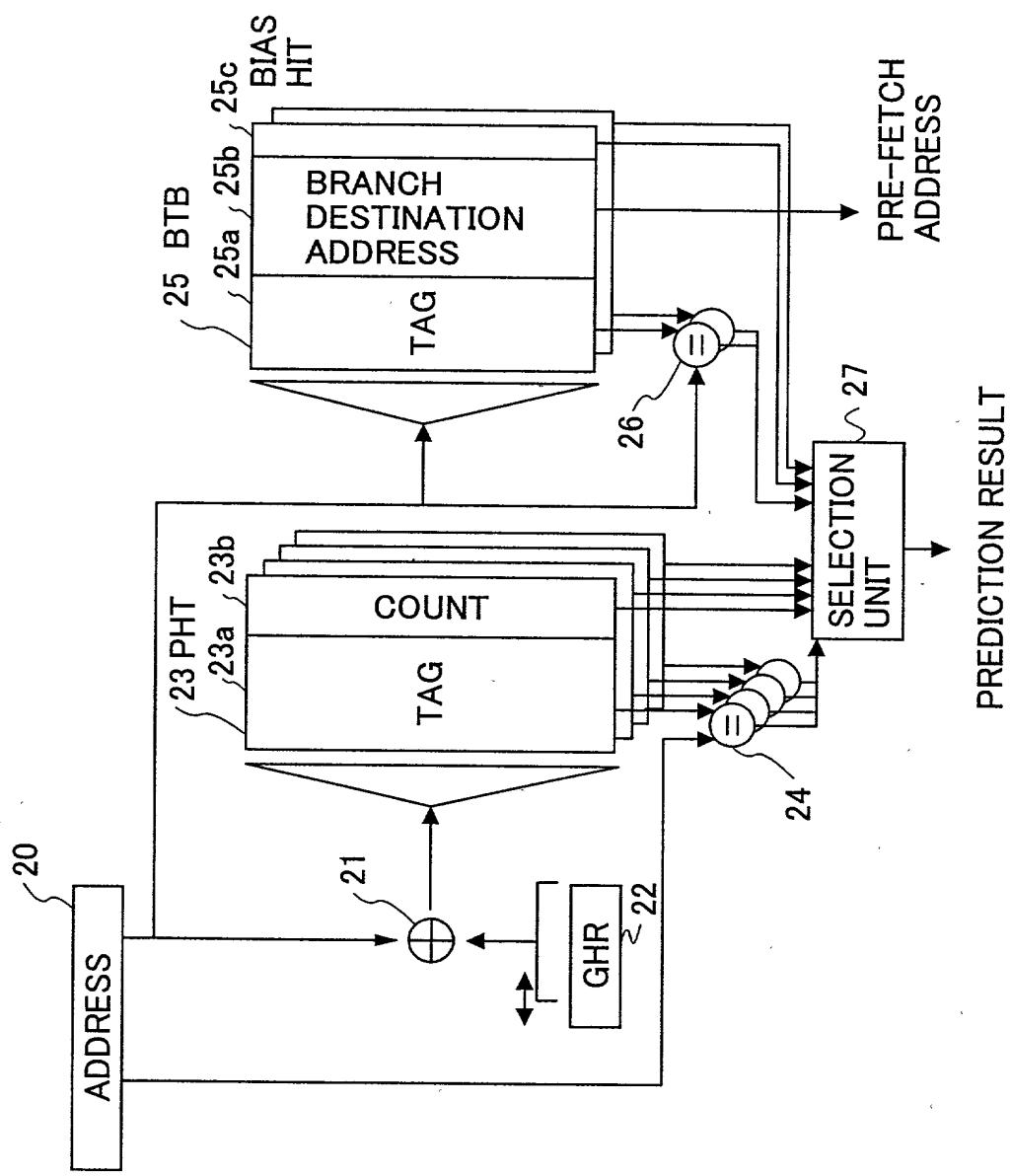


FIG.3

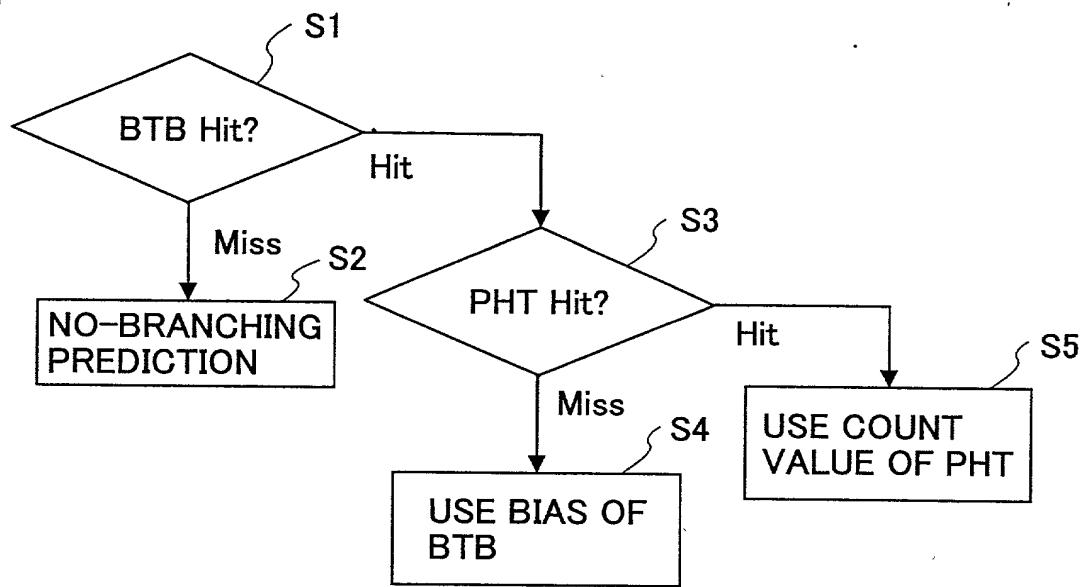


FIG.4

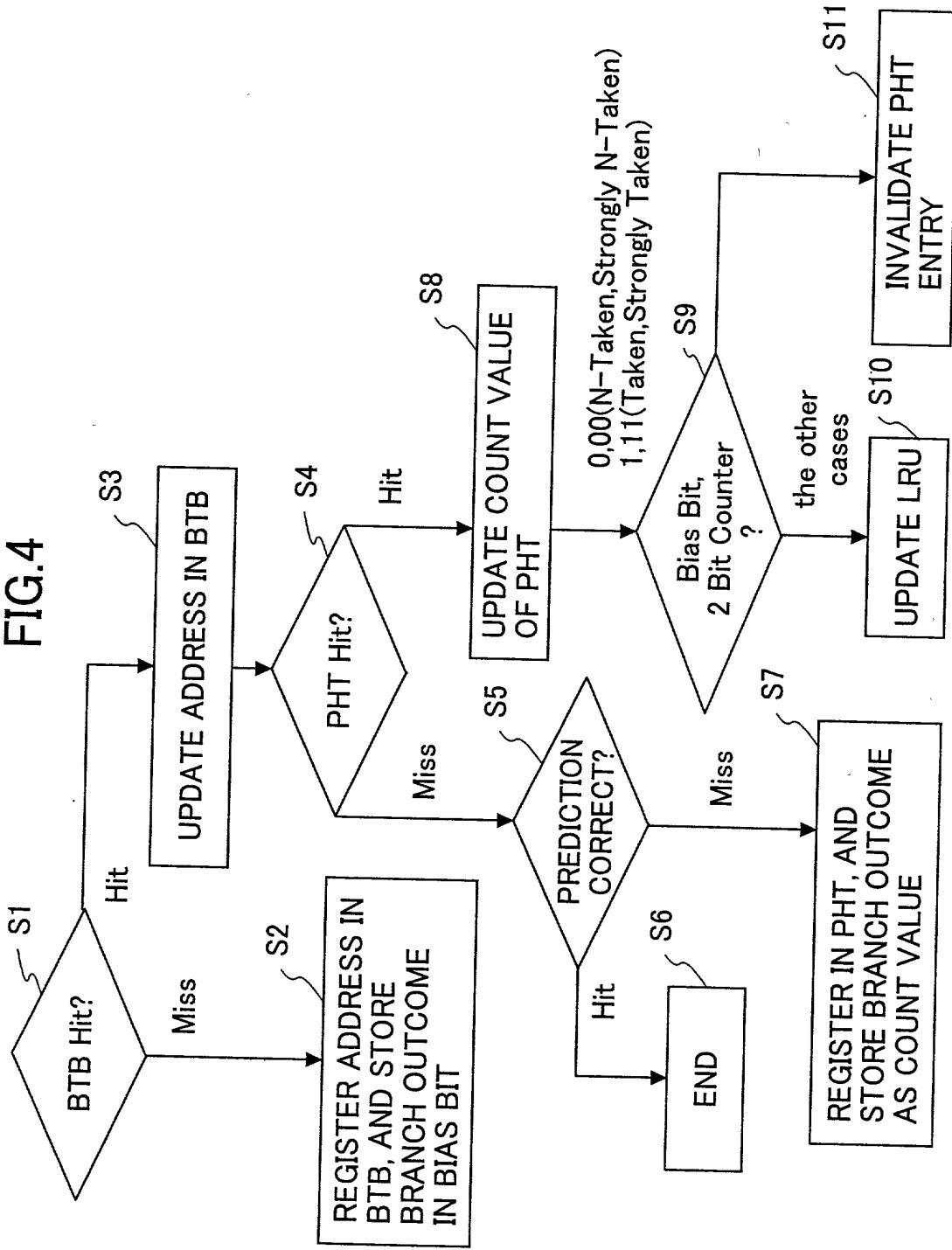


FIG.5

ADDRESS	INSTRUCTION
000000	ARITHMETIC / LOGIC OPERATION INSTRUCTION
000001	BRANCH INSTRUCTION, BRANCHING TO 000011
000010	ARITHMETIC / LOGIC OPERATION INSTRUCTION
000011	ARITHMETIC / LOGIC OPERATION INSTRUCTION
000100	BRANCH INSTRUCTION
000101	BRANCH INSTRUCTION
000110	BRANCH INSTRUCTION
000111	BRANCH INSTRUCTION
001000	BRANCH INSTRUCTION
001001	ARITHMETIC / LOGIC OPERATION INSTRUCTION
001010	ARITHMETIC / LOGIC OPERATION INSTRUCTION
001011	ARITHMETIC / LOGIC OPERATION INSTRUCTION
001100	BRANCH INSTRUCTION, BRANCHING TO 001001 (BRANCHING FIVE TIMES, FOLLOWED BY NO BRANCHING)
001101	ARITHMETIC / LOGIC OPERATION INSTRUCTION
001110	ARITHMETIC / LOGIC OPERATION INSTRUCTION
001111	BRANCH INSTRUCTION, BRANCHING TO 000010
010000	ARITHMETIC / LOGIC OPERATION INSTRUCTION

FIG.6

